A34488 (071308.0211) PATENT

## <u>REMARKS</u>

By this Preliminary Amendment originally-filed claims 1-14 have been cancelled and replaced with the new claims 15-28 which are believed to place the claims in better form for consideration upon examination. No new matter has been added.

The amendments to the original specification are provided to address grammatical and syntactical errors in the translation of the original German language specification. No new matter has been introduced into the application.

The amendments to the "Claims" and "Abstract" are reflected in the attached "Version With Marked Changes Made."

Favorable consideration and allowance of the claims are respectfully requested.

Respectfully submitted,

Dated: November 19, 2001

Bradley B. Geist Reg. No. 27,551

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## Version With Marked Changes Made

WE CLAIM:

1.		<sup>253</sup> 4	254 		<u>15.</u> A	method of o	perating	<sup>255</sup> an <sup>256</sup> <u>a</u>
programn	<u>ned</u> ind	ustrial (	controller	equipped	with a r	untime syst	em <sup>257</sup> in (	articular
for <sup>258</sup> <u>a</u> p	roduction	on <sup>259</sup> m	nachines,	wherein r	nechani	sms which	enable <sup>260</sup>	<u>machine</u>
comprisin	ng		the	<u> </u>		steps		of
	·	che	cking for	occurrer	ice of	a <sup>261</sup> user	to wait <sup>26</sup>	<sup>32</sup> desired
condition	in	the	<sup>263</sup> progra	ım flow	<sup>264</sup> opera	ation of	the r	<u>nachine,</u>
		stor	pping the	program	flow of	the machin	e operati	on while
checking	for <sup>265</sup> a	ny <sup>266</sup> th	e occurre	nce of sa	<u>id</u> desir	ed <sup>267</sup> condi	tion-are p	<del>orovided,</del>
the prog	<del>yram f</del> l	<del>ow be</del>	oing imm	ediately	continue	ed when	the cond	<del>lition is</del>
satisfied <sup>26</sup>	<sup>68</sup> condit	<u>ion</u> and	l <sup>269</sup> the pro	ogram flov	/ being :	stopped wh	en the co	ndition is
not satisfied, until it is established that the condition has been satisfied, 270 waiting								
for		said		conditio	1	to		occur,
		incr	easing the	e priority	of <sup>271</sup> the	-checking	for the <sup>27</sup>	<sup>'2</sup> desired
condition	<sup>273</sup> bein	g-incre	ased in (	compariso	n with <sup>27</sup>	′⁴ <u>relative to</u>	the curr	ent task
priority	<sup>275</sup> wh	ile	waiting —	for <sup>276</sup> in	<u>the</u>	program	<u>flow,</u>	and
	. 100	imn	nediately o	ontinuing	the proc	gram flow u	pon satisf	action of
the condit	tion <sup>277</sup> -te	o <del>be sa</del>	tisfied.					

2. <sup>278</sup>2. <sup>279</sup>16. The method according to claim <sup>280</sup>1, <sup>281</sup>15, wherein once the condition has been satisfied, the following program sequence

is processed with high priority up to an explicit end, the old task priority being resumed after the explicit end of the program sequence.

- 3. <sup>282</sup>3. <sup>283</sup>17. The method according to claim <sup>284</sup>1, <sup>285</sup>15, wherein process signals and/or internal signals of the controller and/or variables from user programs are used for the formulation of the conditions.
- 4. <sup>286</sup>4. <sup>287</sup>18. The method according to claim <sup>288</sup>1, <sup>289</sup>15, wherein the conditions contain logical and/or arithmetic and/or any desired functional combinational operations.
- 5. <sup>290</sup>5. <sup>291</sup>19. The method according to claim <sup>292</sup>1,<sup>293</sup>15, wherein <sup>294</sup>a<sup>295</sup>the user program for the operation of the controller <sup>296</sup>contains <sup>297</sup>is capable of responding in the manner set forth more than one such <sup>298</sup>mechanism <sup>299</sup>condition</sup>.
- 6. <sup>300</sup>6. <sup>301</sup>20. The method according to claim <sup>302</sup>1, <sup>303</sup>15, wherein <sup>304</sup> in the operation of the controller, there <sup>305</sup>may be <sup>306</sup>are provided for the controller, a plurality of user programs which <sup>307</sup>contain these mechanisms <sup>308</sup>operate in the manner set forth.
- 7. 3097. 31021. The method as claimed according to claim 3111,31215, wherein the 313 respective mechanism 314 program for operating the controller is available 315 to a user in a user program—as a customary programming-language construct.

level;

level;

user

user

and

329

- 8. 3168. 31722. An industrial controller for carrying out the method according to claim 3181,31915, wherein the runtime system of the controller contains a running level model which has a plurality of running levels of different types with different priority, 320the following 321 said running levels 322 being provided 323 comprising:
- of levels with synchronously a) aroup clocked <sup>324</sup>comprising <sup>325</sup>having at least one system level and at least one user level, <sup>326</sup>-it being possible for the levels of this group of levels 327 to have prioritizing 328 being capable being prioritized with respect to one another: b) а user level for system exceptions: c) time-controlled level: а user event-controlled d) level; an user
- <u>and wherein</u> user levels of the group of levels a) <sup>330</sup> optionally being <sup>331</sup> <u>are</u> able to run synchronously in relation to one of the system levels of the group of levels a).

sequential

cyclical

а

а

9. <sup>332</sup>9. <sup>333</sup>23. The industrial controller according to claim <sup>334</sup>8, <sup>335</sup>22, wherein the basic clock of the running level model is derived from <sup>336</sup>any one of an internal timer <sup>337</sup> or from <sup>338</sup>, an internal clock of a communication medium <sup>339</sup> or from <sup>340</sup>, an external device or <sup>341</sup>from a variable which belongs to the technological process <sup>342</sup> of the machine.

e)

f)

- 10. 343 10. 344 24. The industrial controller according to claim 345 8, 346 22, wherein the time-controlled user level, the event-controlled user level, the sequential running level, the cyclical background level and the user level for system exceptions are optional.
- 11. 347 11. 348 25. The industrial controller according to claim 349 8, 350 22, wherein the synchronous levels are clocked in relation to the basic clock with a step-up and/or step-down ratio and/or in the ratio 1:1.
- 12. <sup>351</sup>12. <sup>352</sup>26. The industrial controller according to claim <sup>353</sup>8, <sup>354</sup>22, wherein further prioritizing stratifications are provided within the running levels.
- 13. <sup>355</sup>13. <sup>356</sup>27. The industrial controller according to claim <sup>357</sup>8, <sup>358</sup>22, wherein user tasks can optionally be run through during system running-up and/or during system running-down.
- 14. <sup>359</sup>14. <sup>360</sup>28. The industrial controller according to claim <sup>361</sup>8, <sup>362</sup>22, wherein user programs which, depending on the type of user level, are programmed in a cycle-oriented or sequential manner can be loaded into the user levels.

### ABSTRACT OF THE DISCLOSURE

Mechanisms for operating an industrial controller <sup>363</sup>(S)-equipped with a runtime system <sup>364</sup> (RTS), in particular for production machines, which enable a user to wait in the program flow for any desired condition are provided <sup>365</sup>, the <sup>366</sup>. The program flow <sup>367</sup>being <sup>368</sup>is immediately continued when the condition is satisfied and the program flow <sup>369</sup>being <sup>370</sup>is stopped when the condition is not satisfied, until it is established that the condition has been satisfied <sup>371</sup>, the <sup>372</sup>. The priority of the checking for the condition <sup>373</sup>being <sup>374</sup>is increased in comparison with the current task priority while waiting for the condition to be satisfied. When the condition has been satisfied, a defined program sequence is processed with high priority up to an explicit end, the old task priority being resumed after the explicit end of the program sequence.



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#### TO ALL WHOM IT MAY CONCERN:

Be it known that WE, Armin Amrhein, Johannes Birzer, Thomas Hennefelder, Martin Kiesel, Raimund Kram and Regina Schmitt, citizens of Germany, residing in Kuemmersbruck, Stulln, Sugenheim, Poxdorf, Erlangen and Erlangen respectively, whose post office addresses are Dresdnerstr. 16, 92245 Kuemmerstruck, Germany; Friedhofweg 2, 92551 Stulln, Germany; Deutenheim 35, 91484 Sugenheim, Germany; Jahnstr. 36, 91099 Poxdorf, Germany; Fliederstr. 7A, 91056 Erlangen, Germany; and Herbstaeckerweg 5, 91056 Erlangen, Germany; respectively, have invented an improvement in:

<sup>3</sup>METHOD OF OPERATING AN-INDUSTRIAL CONTROLLER AND METHOD OF OPERATING SAME of which the following is a

#### **SPECIFICATION**

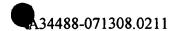
## <sup>5</sup>BACKGROUND OF THE INVENTION

# <sup>6</sup>FIELD OF THE INVENTION

The invention relates to a method of operating an industrial controller, in particular for production machines. The invention also Furthermore, the invention relates to an industrial controller for carrying out the method according to the invention.

[0002] Furthermore, the invention relates to an industrial controller for carrying out the method according to the invention.

[11 This application is related to U.S. Patent Application Ser. No. 09/938,751, filed August 24, 2001 by the present inventors.



## <sup>12</sup>BACKGROUND OF THE INVENTION

[0003] It is customary nowadays, both for stored-program control (SPC) and for motion control (MC), to model hierarchical running levels that are different in each case and are assigned software tasks for controlling the respective technical process. These tasks may perform system functions, but may also be user-programmed.

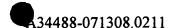
[0004] It is known from DE 197 40 550 A1 that process control functionalities of the stored-program controllers "SPC" and motion functionalities of MC controllers can be integrated in a uniform configurable control system.

This SPC/MC integration takes place in the form of interconnecting SPC and MC control modules. However, when the integration is carried out in such a way, an optimum and efficient task structure is not achieved for the entirety of the control tasks. Furthermore, with this type of integration it is mainly the classic MC functionalities, as are relevant in particular for machine tools, that are supported. Requirements for the controller, as they are known from the operation of production machines, are not optimally supported by this type of interconnection of SPC and MC control modules.

[0006] It is known from EP 0 735 445 A2 to use a separate waiting command (WAIT) for the operation of a machine tool or a robot. However, the waiting command (WAIT) described here still does not optimally support the control of production machines in particular.

[0007] In the application DE 19 93 19 33.2 it is proposed to use the clock of the communication system between the PC system and the peripheral devices for a change between a real-time operating program and a non-real-time operating program. Here, however, it is the task of this clock pickup from the communication system to allow the smoothest possible change to take place between real-time and non-real-time applications

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in an industrial process. In this configuration, the basic clock is only derived however from the clock of the communication medium and it is only used for the changing of the operating system mode of a PC system.

## <sup>13</sup>SUMMARY OF THE INVENTION

The invention <sup>14</sup> is therefore <sup>15</sup> based on the <sup>16</sup> has as its object <sup>17</sup> of <sup>18</sup> the <sup>19</sup> ereating <sup>20</sup> creation in a simple manner <sup>21</sup> of an industrial controller <sup>22</sup> with optimum distinctive characteristics <sup>23</sup> of an industrial controller for <sup>24</sup> in each case different control tasks and different boundary conditions or requirements of the underlying technical process, <sup>25</sup> the controller providing both SPC and MC functionality and consequently also being suitable for the control of production machines.

These optimum distinctive characteristics are achieved in principle on the one hand by a uniform configurable running level model for the control tasks of the industrial controller and on the other hand by mechanisms (<sup>26</sup>e.g., wait\_for\_condition <sup>27</sup>eommand<sup>28</sup>commands) which enable a user to wait for any desired conditions and respond with higher priority in the program flow.

providing mechanisms which enable a user to wait in the program flow for any desired condition, the program flow being immediately continued when the condition is satisfied and the program flow being stopped when the condition is not satisfied, until it is established that the condition has been satisfied, the priority of the checking for the condition being increased in comparison with the current task priority while waiting for the condition to be satisfied. This mechanism makes it possible to express a unified and closed task definition in a piece of code of a user program without further mechanisms, such as for example event handlers, being required. A user can consequently formulate COMPARISON

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the waiting for high-priority events in a sequential program sequence on a relatively low priority level of a "motion task" by program constructs in his program flow (user program), without having to change into another program. This on the one hand avoids a management overhead in the controller, which directly enhances the system performance, and on the other hand supports the locality principle from a programming viewpoint, as a result of which, for example, debugging is made easier.

[0011] The mechanism described and the associated command are referred to hereafter as the "wait for condition".

[0012] A first advantageous refinement of the present invention is that, once the condition has been satisfied, the following program sequence is processed with high priority up to an explicit end, the old task priority being resumed after the explicit end of the program sequence. As a result, high deterministics are achieved in the sequence "waiting for external event" and the "action which follows this event", for example corrective movements in the case of printed mark synchronization. A user consequently has the possibility of temporarily switching to a high priority level in his programs and thereby being able to describe deterministic processes easily and elegantly. Application examples are, for example, printed mark synchronization and a rapid start of movement (for example after an edge change).

[0013] A further advantageous refinement of the invention is that process signals and/or internal signals of the controller and/or variables from user programs are used for the formulation of the conditions. This makes it possible for the user when describing the conditions to use not only user program variables but also directly system states and process signals in a uniform way.

[0014] A further advantageous refinement of the invention is that the conditions contain logical and/or arithmetic and/or any desired functional combinational operations. It is consequently possible for the user to specify complex synchronization relationships within an instruction.

[0015] A further advantageous refinement of the invention is that a user program for the operation of the controller contains more than one such wait-for mechanism. As a result, the flexibility and possibilities for the user, in particular with regard to the description of synchronization activities, in the programming of the applications are increased.

[0016] A further advantageous refinement of the invention is that, in the operation of the controller, there may be a plurality of user programs which contain these <sup>30</sup>wait-for mechanisms. As a result, the flexibility and possibilities for the user, in particular with regard to the description of synchronization activities in the programming of the applications are increased.

A further advantageous refinement of the invention is that the respective 

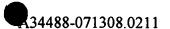
National Maintenance of the invention is that the respective 

National Maintenance of the invention is that the respective 

wait-for mechanism is available to a user in a user program as a customary 
programming-language construct. The "wait\_for\_condition command", which triggers 
this mechanism, can consequently be used by a user in the user programs, for example 
like a while loop, whereby the programming is made very much easier.

[0018] A further advantageous refinement of the invention is that the runtime system of the controller contains a running level model which has a plurality of running levels of different types with different priority, the following running levels being provided:

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belongs to the technological process allows direct feedback from the technological process to the controller to be obtained in a very easy way.

[0021] A further advantageous refinement of the invention is that the time-controlled user level, the event-controlled user level, the sequential running level, the cyclical background level and the user level for system exceptions are optional. As a result, the user can very flexibly create for himself a controller which is very efficient for his actual requirements and which contains the running levels required at the specific time, and consequently does not include any unnecessary overhead.

[0022] A further advantageous refinement of the invention is that the synchronous levels are clocked in relation to the basic clock with a step-up and/or step-down ratio and/or in the ratio 1:1. As a result, the levels can in each case be clocked very easily to a multiple of the basic clock or else be clocked in each case to a reciprocal multiple. On the basis of a common starting variable, step-up ratios or else step-down ratios can consequently be achieved very easily for the respective levels.

[0023] A further advantageous refinement of the invention is that further prioritizing stratifications are provided within the running levels. As a result, the software structure of the industrial controller can be adapted optimally to the different control tasks or to the requirements of the underlying technical process. Consequently, for example, different causes of faults can be assigned to different levels, with for example 39, ascending priority.

[0024] A further advantageous refinement of the invention is that user tasks can optionally be run through during system running-up and/or during system running-down.

This allows, for example, initialization functions to be 40 initiated 41 started during system

- a) a group of levels with synchronously clocked levels, comprising at least one system level and at least one user level, it being possible for the levels of this group of levels to have prioritizing with respect to one another;
- b) a user level for system exceptions;
- c) a time-controlled user level;
- d) an event-controlled user level;
- e) a sequential user level;
- f) a cyclical user level, user levels of the group of levels a) optionally being able to run synchronously in relation to one of the system levels of the group of levels a).

[0019] A major advantage of this stratification is that the communication between the tasks of the process controller (SPC) and those of the motion controller (MC) is minimized. A further advantage is that the programming of the control tasks for the process controller and for the motion controller can take place in a uniform programming language with a uniform creation interface and that the user can flexibly create a running level model tailor-made for his respective requirements.

of the running level model is derived from <sup>32</sup> any of an internal timer <sup>33</sup> or from <sup>34</sup> an internal clock of a communication medium <sup>35</sup> or from <sup>36</sup> an external device or <sup>37</sup> from a variable which belongs to the technological process. As a result, the basic clock for the running level model can be derived in a very flexible and very easy manner. The fact that the basic clock for the running level model can also be derived from a variable which



running-up or <sup>42</sup>it-to <sup>43</sup>be ensured <sup>44</sup>ensure during system running-down that the axes present in the system assume a defined position.

which, depending on the type of user level, are programmed in a cycle-oriented or sequential manner can be loaded into the user levels. This allows the user to adapt the functionality of the controller very flexibly to the underlying requirements of the technical process in his user programs and also allows him to load the user programs into different user levels, in order in this way to achieve distinctive characteristics of the controller that are effective for his respective applications. A further advantage is that the user can load both cycle-oriented user programs and event-oriented user programs into a uniform running level model or runtime system of an industrial controller. A user can consequently additionally load programs programmed according to different paradigms (cycle-oriented for SPC functionality and event-oriented or sequentially for motion functionality) very flexibly and conformally into the user levels of the running level model.

# 45BRIEF DESCRIPTION OF THE DRAWINGS

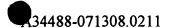
[0026] An exemplary embodiment of the invention is <sup>46</sup>explained <sup>47</sup>described below <sup>48</sup>and represented in <sup>49</sup>conjunction with the <sup>50</sup>drawing <sup>51</sup>appended drawings, in which:

<sup>52</sup>Figure <sup>53</sup>Fig. 1 shows the main running levels of a classic stored-program controller;

<sup>54</sup>Figure <sup>55</sup>Fig. 2 shows the main running levels of a motion controller;

<sup>56</sup>Figure <sup>57</sup>Fig. 3 <sup>58</sup>shows <sup>59</sup>is a schematic representation of an industrial

controller; COMPARISON NY02: 1342250.12342250.2



<sup>60</sup>Figure <sup>61</sup>Fig. 4 shows the running level model of the industrial controller according to the invention;

<sup>62</sup>Figure <sup>63</sup>Fig. 5 shows an exemplary embodiment of the loading of user programs into the user levels;

<sup>64</sup>Figure <sup>65</sup>Fig. 6 shows an exemplary embodiment of the use and mechanism of the wait for condition command in the running level model of the industrial controller according to the invention;

<sup>66</sup>Figure <sup>67</sup>Fig. 7 shows a further exemplary embodiment of the use and mechanism of the wait for condition command in the running level model of the industrial controller according to the invention;

<sup>68</sup>Figure <sup>69</sup>Fig. 8 shows the syntactic description of the wait for condition command in a syntax diagram;

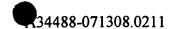
<sup>70</sup>Figure <sup>71</sup>Fig. 9 shows an example of the formulation of an expression in programming-language notation; and

<sup>72</sup>Figure <sup>73</sup>Fig. 10 shows in a schematic representation possibilities for <sup>74</sup>how<sup>75</sup>obtaining the basic clock <sup>76</sup>is obtained for the industrial controller.

# <sup>77</sup>DETAILED DESCRIPTION OF THE INVENTION

In <sup>78</sup>the representation according to Figure <sup>79</sup>Fig. 1, the main running levels [0027] of a classic stored-program controller (SPC), arranged according to their priority, are shown. The increase in priority is symbolized there by <sup>80</sup>an <sup>81</sup>the upwardly pointing arrow. In the lowest-priority level, 82 as indicated by a dashed line, two different tasks are performed, 83 to be specific 84 as indicated by the dashed line. Specifically, these are a free cycle, i.e. 85 "user level free cycle" and a background system level, i.e. 86 "system level background". The background system level is assigned, for example, communication

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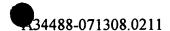


parameters for the calling clock of the tasks or of the programs of this level can be parameterized. Monitoring takes place to ascertain whether the processing of a user program of this clocked level has been completed in time before the start event occurs once again. If the clock time elapses without the user program of the assigned level being processed to completion, a corresponding task of a next-but-one, in priority terms, "user level for asynchronous faults" is started. In this "user level for asynchronous faults", the user can program out the handling of fault states.

The "user level time-controlled" is followed by a "user level events". The response to external or internal events takes place within the "user level events". A typical example of such an event is the switching of a binary or digital input, whereby typically an event is triggered. In a "system level high priority" lie the tasks of the operating system which ensure the operating mode of the programmable controller (SPC).

[0029] The representation according to <sup>88</sup>Figure <sup>89</sup>Fig. 2 shows the main running levels of a motion controller (MC). Here, too, the individual levels are arranged hierarchically according to their priority, as symbolized by <sup>90</sup>em <sup>91</sup>the upwardly arrow. A "system level background" and a "user level sequential" have an equal priority, that is the lowest priority. This unified nature in terms of tasks is symbolized as in <sup>92</sup>Figure <sup>93</sup>Fig. 1 by a dashed line. The tasks of the "user level sequential" are processed together with the tasks of the "system level background" in the round-robin procedure. Typical tasks of the "system level background" are, for example, those for communication task. In the "user level sequential", the parts of the program programmed by the user run for the actual control task. If, in one of these parts of the program, the controller encounters a

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movement or positioning command, a "suspend" is set, i.e. <sup>94</sup>, the user program is interrupted at this point. In this case, a command is synchronously used. The processing of this movement or positioning command take place in a highest-priority "system level clocked". Each and every position controller or interpolator which is running in the "system level clocked" executes this movement or positioning command. After execution of the command, control returns to the "user level sequential" and the user program interrupted by "suspend" is continued by a "resume" at the same point. The "system level clocked" contains not only the already mentioned position controllers but also the interpolation part of the control.

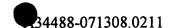
[0030] The "user level events" resumes at the lowest-priority level.

Accommodated here are those tasks which respond to external or internal events. Such events may be alarms, for example.

In a following "user level synchronously clocked", synchronously clocked user tasks are performed, for example controller functionalities. These tasks are synchronized in relation to clocked system functions, such as for example the interpolator, position controller or cyclical bus communication.

In <sup>95</sup>the representation according to Figure <sup>96</sup>Fig. 3, <sup>97</sup>it <sup>98</sup>there is shown <sup>99</sup>, in the form of a <sup>100</sup>block structure diagram <sup>101</sup>, that the control of a technical process P1 is performed by means of the runtime system RTS of an industrial controller. The connection between the runtime system RTS of the controller and the technical process P1 takes place bidirectionally via the inputs/outputs EA. The programming of the controller, and consequently fixing the behavior of the runtime system RTS, takes place in the engineering system ES. The engineering system ES contains tools for configuring, project planning and programming for machines or for controlling technical processes.

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The programs created in the engineering system are transferred via the information path I into the runtime system RTS of the controller. With respect to its hardware equipment, an engineering system ES usually comprises a computer system with graphics screen (102 for example display), input aids (for example 103 keyboard and mouse), processor, main memory and secondary memory, a device for accepting computer-readable media (for example 104 floppy disks, CDs) and also connection units for data exchange with other systems (for example 105 further computer systems, controllers for technical processes) or media (for example 106 the Internet). A controller usually comprises input and output units, and also a processor and program memory. It is also conceivable for the control of a technical process P1 to be performed by means of a plurality of runtime systems RTS of industrial controllers.

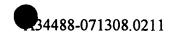
The representation <sup>107</sup>according to Figure <sup>108</sup>of Fig. 4 shows the running level model of the industrial controller <sup>109</sup>according to the invention. The prioritizing of the levels is indicated by <sup>110</sup>an <sup>111</sup>the arrow <sup>112</sup>pointing upwardly in the direction of the highest priority. The lowest-priority levels are the "cyclical user level" and the "sequential user level". These two levels run with the same priority. Therefore, these levels are separated in the representation according to <sup>113</sup>Figure <sup>114</sup>Fig. 4 by a dashed line. The "cyclical user level" includes the "background task", which is cycle-time-monitored. In the "sequential user level", the "motion tasks" are run through. "Motion tasks" are not cycle-time-monitored and serve essentially for describing sequential sequences. "Motion tasks" are processed virtually in parallel. Generally, all the user levels contain one or more tasks. The tasks receive the user programs. The tasks of the "cyclical user level" and of the "sequential user level" are processed in a common round-robin cycle.

The next-following level is the "time-controlled user level". The tasks of this level are activated in a time-controlled manner. The time control can be set in a <sup>115</sup>granularity <sup>116</sup>scale of milliseconds. The "time-controlled user level" is followed by the "event-controlled user level". In this level, after detection of a user interrupt, what are known as "user interrupt tasks" are activated. User interrupt events may be formulated as a logical combination of process events and/or internal states.

The next-higher level is the "user level for system exceptions". In this "user level for system exceptions", monitoring <sup>117</sup>of system interrupts is carried out <sup>118</sup> of system interrupts, the <sup>119</sup>. The occurrence of <sup>120</sup>which <sup>121</sup>system interrupts has the effect of generating what are known as "exceptions", i.e. <sup>122</sup> instances of handling exceptional cases. In the "user level for system exceptions" there are, for example, the following tasks, which are activated when a corresponding system interrupt occurs:

- a) "time fault task", which is activated when time monitors respond;
- b) "peripheral fault task", which is activated for example in the event of process and diagnosis alarms, but also in the event of station failure or station return;
- c) "system fault task", which is activated in the event of general system faults;
- d) "program fault task", which is activated in the event of programming faults (for example division by zero);
- e) "time fault background task", which is activated when the cycle time monitoring of the background task responds; and
- f) "technological fault task", which is activated in the event of technological faults.

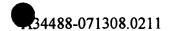
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[0036] Following next is the group of levels "synchronously clocked levels". This group of levels has the highest priority in the running level model. The individual levels of this group of levels may <sup>123</sup>have <sup>124</sup>be further <sup>125</sup>prioritizings <sup>126</sup>prioritized with respect to one another. The group of levels "synchronously clocked levels" comprises at least one system level and at least one user level. The system levels include the system functions such as, for example, position controller or interpolator. User programs (AP1 - AP4; <sup>127</sup>Figure <sup>128</sup>Fig. 5) can be flexibly loaded in addition by a user into the user levels of this group of levels.

[0037] For the clock control of the "synchronously clocked levels" there are a <sup>129</sup> series <sup>130</sup> number of different possibilities for clock generation. The basic clock may come, for example, from an internal timer (T1; <sup>131</sup>Figure <sup>132</sup>Fig. 10) or from an internal clock (T3; 133 Figure 134 Fig. 10) of a communication medium (for example Profibus) or else the clock may also be derived from a process event of the technological process. Such a process event may be, for example, the clock rate (TG; 135 Figure 136 Fig. 10) of an operation on a production machine or packaging machine. User levels of the group of levels "synchronously clocked levels" may in this case be clocked on the basis of the basic clock, but they may also run synchronously in relation to one of the system levels of the group of levels "synchronously clocked levels". The user tasks of this user level synchronous to a system level consequently have a synchronous, i.e. <sup>137</sup>, deterministic, relationship with a system level which can be flexibly fixed by the user. This has the advantage that deterministic responses to system tasks (system tasks run in the system levels) which the user has programmed in his user tasks, which run in the user levels of the group of levels "synchronously clocked levels", are guaranteed by the system. That is to say, for example, that the system guarantees that this "synchronous user level" is

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correspondingly activated for example before the interpolator, or else before any other desired system function.

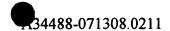
[0038] The "time-controlled user level", the "event-controlled user level", the "sequential user level", the "cyclical user level" and the "user level for system exceptions" are optional.

The task of the "cyclical user level" (background task) is cycle-timemonitored. The "motion tasks", on the other hand, are not cycle-time-monitored and serve essentially for describing sequential sequences. That is to say the present running level model supports a user both in the programming of sequential sequences and in event programming. Consequently, synchronous events and asynchronous events can be covered by the programming. The user programs (AP1 - AP4; <sup>138</sup>Figure <sup>139</sup>Fig. 5) created by the user can be loaded in addition into the user levels. The user programs AP1 to AP4 are usually created with the aid of a programming environment of the engineering system (ES; <sup>140</sup>Figure <sup>141</sup>Fig. 3).

exemplary embodiment of the additional loading of user programs into the user levels.

146 Figure 147 Fig. 5 shows by way of example distinctive characteristics of user levels of the running level model. 148 It is 149 As shown by the three 150 points 151 bold dots at the lower edge of the drawing 152 that 153, there may also be still further user levels, or else system levels. The prioritizing of the levels is indicated as above by 154 and 155 the arrow 156 pointing upwardly in the direction of the highest priority. The user levels are assigned the user programs AP1 to AP4, indicated at the right-hand edge of the figure by small squares. The assignment is shown by assignment arrows ZP1 to ZP4. In the user levels

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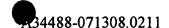
AP<sup>158</sup>4. 159</sup>4. respectively. These tasks are then run through or processed in accordance with a specific strategy (for example sequentially). They may continue to have the property that they are run-time-monitored.

<sup>160</sup>The representation according to Figure <sup>161</sup>Fig. 6 shows an exemplary [0041] embodiment of the use and mechanism of the wait for condition command, in the running level model of the industrial controller according to the invention. The wait for condition command (represented in <sup>162</sup>Figure <sup>163</sup>Fig. 6 as wait\_for\_cond()) is used by way of example in this representation in the "sequential user level". The wait for condition command is used in the "motion tasks" MT1 and MT2 created by the user, which are a component part of the "sequential user level". The "motion tasks" MT1 and MT2 are in a round-robin cycle, represented by the arrow from MT1 to MT2 and by the <sup>164</sup>angled away <sup>165</sup>looping return arrow from MT2 to MT1. The three <sup>166</sup>points inside this 167 bold dots in the return arrow indicate that there may be still further "motion tasks" in the round-robin cycle. The "motion task" MT1 contains the wait for condition command "wait for cond(cond 1)", the "motion task" MT2 contains the wait for condition command "wait for cond(cond 2)". 168 Three points used 169 The bold dots included in each case within MT1 and MT2 indicate that, in addition to the two wait for condition commands and the three positioning commands pos1() to pos3(), still further commands may be contained in the "motion tasks".

Altogether, the running level model, represented by way of example in 

170 Figure 171 Fig. 6, of a runtime system for an industrial controller comprises the 
following levels (enumeration from the lowest to the highest priority): "cyclical user 
level", "sequential user level" (the tasks of these two levels have the same priority, 
represented by the dashed line between these levels), "time-controlled user level", "event-

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controlled user level", "user level for system exceptions", "synchronously clocked user level 2", "synchronously clocked user level 1", "synchronously clocked system level 2" and, as the highest-priority level, a "synchronously clocked system level 1".

The operating mode of the wait\_for\_condition command is shown by way of example by "wait\_for\_cond(cond\_1)" from the "motion task" MT1. If the "motion task" MT1 is next in turn in the round-robin cycle, the commands of the "motion task" MT1 are serviced until the time slice has elapsed, or an interruption occurs. If this is the case, the "motion task" MT2 is serviced as the next task in the cycle, etc. If the wait\_for\_cond(cond\_1) command is processed in the "motion task" MT1, the condition cond\_1 is checked. If cond\_1 = true, that is to say is satisfied, the next-following command pos2() is immediately executed and, if appropriate, further commands present in MT1 are successively processed, until control is passed to the next task.

If the condition cond\_1 = false, that is to say is not satisfied, the "motion task" MT1 is immediately interrupted and MT2 is serviced in the round-robin cycle. The condition cond\_1 is inserted, however, into the "synchronously clocked system level 2" (indicated by the solid <sup>172</sup>angled away <sup>173</sup>line arrow from the wait\_for\_cond(cond\_1) command to the "synchronously clocked system level 2") and is checked in the clock cycle of this system level to ascertain whether it has been satisfied. If the condition cond\_1 is satisfied, the current task is displaced in the round-robin cycle, i.e. <sup>174</sup>a it has the time slice withdrawn from it and the motion task MT1 is continued immediately after the wait\_for\_cond(cond\_1) with the positioning command pos2(). The return from the "synchronously clocked system level 2" to the positioning command pos2(), i.e. <sup>175</sup>a to the "sequential user level", is indicated by the dashed <sup>176</sup>line arrow.



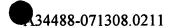
The fact that, when the condition of the wait\_for\_condition command has not been satisfied, the checking for the condition takes place in a high-priority "synchronously clocked system level" and, when the condition has been satisfied, the interrupted "motion task" is continued immediately makes it possible for a user to specify extremely time-critical applications by simple language means during the programming of sequences of movements. The performance and deterministics are further enhanced by only inserting and considering currently applicable conditions when checking the conditions in the respective high-priority "synchronously clocked system levels".

The mechanism described here also does not require an explicit event handler. 

178 The 179 Consequently, the great advantage from the user viewpoint is 
180 eonsequently that the user can now formulate high-priority events in a sequential running program on a relatively low priority level of a "motion task" in his program flow with the aid of program constructs, and does not have to change into another program which he then has to project by means of other mechanisms (for example manually or under interrupt control) onto a synchronous user task 181 Instead, 182 but 183 the 184 instead 185 user has the possibility in a closed user program of formulating this "waiting for high-priority event" and "high-priority reaction" cycle for this event in a program on a closed basis.

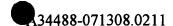
[0047] The conditions which are inquired in a wait\_for\_condition command can be formulated very flexibly and elegantly by the user. For instance, for formulating these conditions, the user can use program variables from a user program or internal variables of the controller, or he can also reference process signals. These variables may then be combined logically, arithmetically or by any desired functions in terms of their content,

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to formulate a condition from them. In addition to the high-priority inquiries as to whether the condition is satisfied, it is also conceivable that, if the condition is satisfied, a program code belonging to it, i.e. <sup>186</sup>, an underlying response, which is user-programmable, is also executed with high priority <sup>187</sup>.—And <sup>188</sup>that <sup>189</sup>and the return to the low-priority level only takes place after execution of this program code.

The representation according to <sup>190</sup>Figure <sup>191</sup>Fig. 7 shows an extended [0048] exemplary embodiment of the use and mechanism of the wait for condition command, in the running level model of the industrial controller according to the invention. The wait for condition command (in <sup>192</sup>Figure <sup>193</sup>Fig. 7 likewise represented as wait for cond()) is used by way of example in this representation in the "sequential user level". The wait for condition command is used in the "motion tasks" MT3 and MT4 created by the user, which are a component part of the "sequential user level". The "motion tasks" MT3 and MT4 are in a round-robin cycle, represented by the arrow from MT3 to MT4 and by the <sup>194</sup>angled-away <sup>195</sup>looping return arrow from MT4 to MT3. The three <sup>196</sup>points inside this <sup>197</sup>bold dots in the return arrow indicate that there may be still further "motion tasks" in the round-robin cycle. The "motion task" MT3 contains the wait for condition command "wait for cond(cond 3)", the "motion task" MT4 contains the wait for condition command "wait for cond(cond 4)". 198 Three points used 199 The bold dots included in each case within MT3 and MT4 indicate that, in addition to the two wait for condition commands and the positioning commands pos4() to pos8(), still further commands may be contained in the "motion tasks". The programming-language constructs "wait for cond()" and "end wait for cond" have the effect of bracketing a program sequence in the "motion tasks". In the "motion task" MT3, the commands pos5() and pos6() are bracketed in this way. The use of "wait for cond()" and



"end\_wait\_for\_cond" is also indicated in the "motion task" MT4. It is schematically indicated by 3 <sup>200</sup>points<sup>201</sup>bold dots in each case in the "motion task" MT4 that further instructions may be present before, within and after the "wait\_for\_cond() - end wait for cond" construct.

The running level model, represented by way of example in

202 Figure 203 Fig. 7, of a runtime system for an industrial controller comprises, as in

204 Figure 205 Fig. 6, the following levels (enumeration from the lowest to the highest priority): "cyclical background level", "sequential user level" (the tasks of these two levels have the same priority, represented by the dashed line between these levels),

108 \*\*event\* 207 time\* controlled user level", "208 time\* 209 event\* controlled user level", "user level for system exceptions", "synchronously clocked user level 2", "synchronously clocked user level 2", a "synchronously clocked system level 2" and, as the highest-priority level, a "synchronously clocked system level 1".

In <sup>210</sup>Figure <sup>211</sup>Fig. 7, the operating mode of the wait\_for\_condition command with an associated program sequence is shown by way of example as wait\_for\_cond(cond\_3)" from the "motion task" MT3. The checking of the condition cond\_3 and the processing of the associated program sequence (bracketed between "wait\_for\_cond(cond\_3)" and "end\_wait\_for\_cond") take place in this case on a higher-priority level of the running level model. The program sequence belonging to "wait\_for\_cond(cond\_3)" is formed by the sequence of the commands pos5() and pos6().

[0051] If the "motion task" MT3 is next in turn in the round-robin cycle, the commands of the "motion task" MT3 are serviced until the time slice has elapsed, or an interruption occurs. If this is the case, the "motion task" MT4 is serviced as the next task in the cycle, etc. If the "wait\_for\_cond(cond\_3)" command is processed in the "motion

task" MT3, the condition cond\_3 is checked. If cond\_3 = true, that is to say is satisfied, the normal program sequence is continued, i.e.<sup>212</sup>, the command pos5() is executed next and, if appropriate, further commands present in MT3 are successively processed, until control is passed to the next motion task.

If the condition cond\_3 = false, that is to say is not satisfied, the "motion task" MT3 is immediately interrupted and MT4 is serviced in the round-robin cycle. The condition cond\_3 and the commands pos5() and pos6() (as the associated program sequence) are processed in the priority of the "synchronously clocked system level 2" (indicated by the solid <sup>213</sup>angled-away<sup>214</sup>line arrow, starting from the <sup>215</sup>parenthesis<sup>216</sup>bracket which expresses the unified nature of wait\_for\_cond(cond\_3), end\_wait\_for\_cond and the associated program sequence, up to the "synchronously clocked system level 2"). <sup>217</sup>Condition\_cond\_3 is checked in the clock cycle of this system level to ascertain whether it has been satisfied. If cond\_3 has been satisfied, the associated program sequence (here: the sequence of the commands pos5() and pos6()) is processed with the priority of the "synchronously clocked system level 2". The return from the "synchronously clocked system level 2" to the positioning command pos7(), i.e. <sup>218</sup> to the "sequential user level", is indicated by the dashed <sup>219</sup>line arrow.

The fact that, when the condition of the wait\_for\_condition command has not been satisfied, the checking for the condition takes place in a high-priority "synchronously clocked system level" and, when the condition has been satisfied, an associated program sequence which can be created by the user is executed on this high-priority system level makes it possible for even extremely time-critical applications to be specified and carried out by simple language means.



One possible application is printed mark synchronization. The aim here is to detect a printed mark on a material with high priority. When this printed mark is detected, typically an actual value is captured ("latching" for example of a position or sensor actual value). On the basis of this captured actual value, a correction value is calculated and impressed on the system as a superposed movement. The process of actual value detection, correction value calculation and implementation of the superposed movement must take place in a deterministic time period. Therefore, this process must take place with high priority.

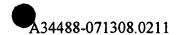
[0055] A further application is the "rapid start of movement". Here, the aim is to detect, for example, an edge change very quickly and then begin a start of movement (for example positioning movement) immediately thereafter. The deterministics of detecting an event and triggering consequent actions are decisive for the productivity of a machine. In the case of production machines, such cyclical processes must take place in a deterministic time, for example <100 ms or <50 ms. When processing the tasks on a normal background level, these deterministics cannot be guaranteed. The mechanism described is particularly suitable for use in the case of machines which have periodic machine cycles.

[0056] The performance is further enhanced by only inserting and considering currently applicable conditions when checking the conditions in the respective high-priority "synchronously clocked system levels".

[0057] As already mentioned in <sup>220</sup>Figure <sup>221</sup>connection with Fig. 6, the mechanism described here does not require an explicit event handler.

<sup>222</sup>The <sup>223</sup>Consequently, the great advantage from the user viewpoint is <sup>224</sup>consequently that the user can now formulate high-priority events in a sequential running program on a

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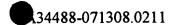
relatively low priority level of a "motion task" in his program flow with the aid of program constructs, and does not have to change into another program which he then has to project by means of other mechanisms (for example manually or under interrupt control) onto a synchronous user task<sup>225</sup>. Instead, <sup>226</sup>but<sup>227</sup>the <sup>228</sup>instead<sup>229</sup>user has the possibility in a closed user program of formulating this "waiting for high-priority event" and "high-priority reaction" cycle for this event in a program on a closed basis.

The wait\_for\_condition command can be used by the user very flexibly and easily, since it is available as a normal programming-language construct. The formulation of the conditions is also flexible and easy for a user. For instance, for formulating these conditions, the user can use program variables from a user program or internal variables of the controller, or he can also reference process signals. These variables may then be combined logically, arithmetically or by any desired functions in terms of their content, to formulate a condition from them.

[0059] The wait\_for\_condition construct provides a user with the possibility in normal user programs for sequences of movements of temporarily switching a user program to a higher priority level, to be able to guarantee deterministic processes.

[0060] 230 The representation according to Figure 231 Fig. 8 shows the programming-language construct of the wait\_for\_condition mechanism as a syntax diagram. The terminal elements are in this case represented with rounded corners: "WAITFORCONDITION", "WITH", "DO", "END\_WAITFORCONDITION" and ";". The non-terminal elements are represented as rectangles: "expression designation", "SWITCH" and "INSTRUCTION PART". The elements "WITH" and SWITCH" are optional.

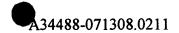
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[0061] 232 The representation according to Figure 233 Fig. 9 shows the use of the wait\_for\_condition construct in a program sequence. In the upper part of 234 Figure 235 Fig. 9, the formulation of the condition "my expression" is represented, in the lower part it is shown how this condition is used in a wait\_for\_condition construct.

[0062] 236 The representation according to Figure 237 Fig. 10 238 shows in 239 is a schematic representation 240 of the possibilities for 241 how 242 obtaining the basic clock 243 is obtained for the industrial controller. 244 Figure 245 Fig. 10 shows 246 by way of example 247 a communication topology into which the controller S is integrated. The controller S is represented by a square. The controller S is connected by a connection line A2 to the bus B1, to which the external device EG is attached via a connection line A1. The connection to the technical process P2 takes place via the bus B2. The technical process P2 is represented at the lower edge of the figure by a rectangle. The controller S is connected via the connection line A3 to the bus B2, which in turn establishes the connection to the technical process P2 via the connection line A4.

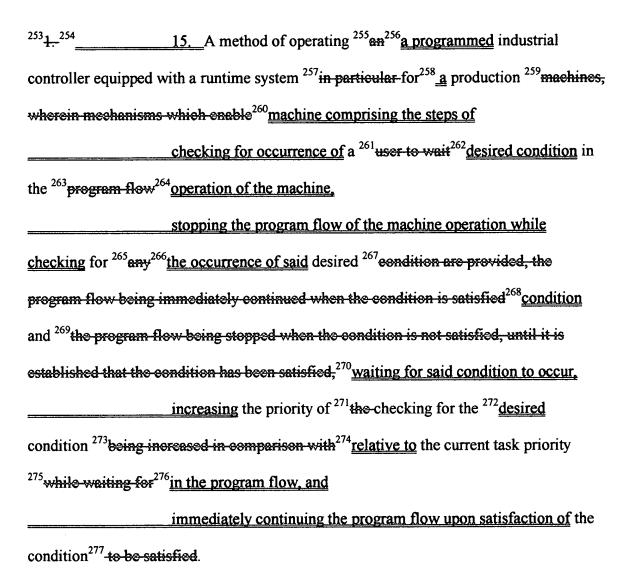
[0063] The generation for the basic clock of the controller S can take place from different clock sources. For example, from an internal clock source, represented by the internal timer T2 of the controller S or else by an external clock source, such as for example the timer T1, which belongs to the external device EG. The basic clock of a communication medium may also serve, however, as an external clock source. If the bus B2 is realized for example by an equidistant Profibus, the clock for the controller can be obtained from the basic clock of this bus. This is represented in <sup>248</sup>figure <sup>249</sup>Fig. 10 by the timer T3 being positioned directly on the connection line A3, and this connection line A3 establishes the connection to the bus B2. The controller <sup>250</sup>S is consequently attached to the bus as a slave and can use the bus clock directly. Furthermore, a clock generator TG



which is integrated in the technical process P2 may serve as an external clock source. A clock generator TG in a technical process may be, for example, the operating cycle of a production machine or packaging machine. In the representation according to 

251 figure 252 Fig. 10, bus connections are represented by way of example as communication media. However, ring, star or other types of connection may also be chosen as communication media, as well as wireless connections. The basic clock mentioned above can then be derived from these connection systems.

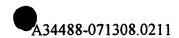
#### WE CLAIM:



<sup>278</sup>2. <sup>279</sup>16. The method according to claim <sup>280</sup>1, <sup>281</sup>15, wherein once the condition has been satisfied, the following program sequence is processed with high priority up to an explicit end, the old task priority being resumed after the explicit end of the program sequence.

<sup>282</sup>3.-<sup>283</sup>17. The method according to claim <sup>284</sup>1, <sup>285</sup>15, wherein process signals and/or internal signals of the controller and/or variables from user programs are used for the formulation of the conditions.

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<sup>286</sup>4. <sup>287</sup>18. The method according to claim <sup>288</sup>1, <sup>289</sup>15, wherein the conditions contain logical and/or arithmetic and/or any desired functional combinational operations.

user program for the operation of the controller <sup>296</sup> contains <sup>297</sup> is capable of responding in the manner set forth more than one such <sup>298</sup> mechanism <sup>299</sup> condition.

operation of the controller, there <sup>305</sup>may be <sup>306</sup>are provided for the controller, a plurality of user programs which <sup>307</sup>contain these mechanisms <sup>308</sup>operate in the manner set forth.

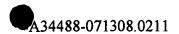
the <sup>313</sup>respective mechanism <sup>314</sup>program for operating the controller is available <sup>315</sup>to a user in a user program-as a customary programming-language construct.

3168. 31722. An industrial controller for carrying out the method according to claim 3181, 31915, wherein the runtime system of the controller contains a running level model which has a plurality of running levels of different types with different priority,

320 the following 321 said running levels 322 being provided 323 comprising:

- a) a group of levels with synchronously clocked levels, <sup>324</sup>eomprising <sup>325</sup>having at least one system level and at least one user level, <sup>326</sup> it being possible for the levels of this group of levels <sup>327</sup>to have prioritizing <sup>328</sup>being capable of being prioritized with respect to one another;
  - b) a user level for system exceptions;
  - c) a time-controlled user level;

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- d) an event-controlled user level;
- e) a sequential user level; and
- f) a cyclical user level; <sup>329</sup>

<u>and wherein</u> user levels of the group of levels a) <sup>330</sup> optionally being <sup>331</sup> <u>are</u> able to run synchronously in relation to one of the system levels of the group of levels a).

the basic clock of the running level model is derived from <sup>336</sup>any one of an internal timer<sup>337</sup> or from <sup>338</sup>, an internal clock of a communication medium <sup>339</sup> or from <sup>340</sup>, an external device or <sup>341</sup>from a variable which belongs to the technological process <sup>342</sup> of the machine.

343 10. 344 24. The industrial controller according to claim 345 8, 346 22, wherein the time-controlled user level, the event-controlled user level, the sequential running level, the cyclical background level and the user level for system exceptions are optional.

wherein the synchronous levels are clocked in relation to the basic clock with a step-up and/or step-down ratio and/or in the ratio 1:1.

35112. 35226. The industrial controller according to claim 3538, 35422, wherein further prioritizing stratifications are provided within the running levels.

35513. 35627. The industrial controller according to claim 3578, 35822. wherein user tasks can optionally be run through during system running-up and/or during system running-down.

wherein user programs which, depending on the type of user level, are programmed in a cycle-oriented or sequential manner can be loaded into the user levels.

ABSTRACT OF THE DISCLOSURE

Mechanisms for operating an industrial controller <sup>363</sup>(S) equipped with a runtime system<sup>364</sup> (RTS), in particular for production machines, which enable a user to wait in the program flow for any desired condition are provided 365, the 366. The program flow 367 being 368 is immediately continued when the condition is satisfied and the program flow 369 being 370 is stopped when the condition is not satisfied, until it is established that the condition has been satisfied<sup>371</sup>, the 372. The priority of the checking for the condition <sup>373</sup>being<sup>374</sup>is increased in comparison with the current task priority while waiting for the condition to be satisfied. When the condition has been satisfied, a defined program sequence is processed with high priority up to an explicit end, the old task priority being resumed after the explicit end of the program sequence.